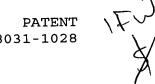
8031-1028



## IN THE U.S. PATENT AND TRADEMARK OFFICE

n re application of: Yoshihiro NONAKA

Conf.: 5218

Appl. No.:

10/648,256

2811 Group:

Filed:

August 27, 2003

Examiner: Ori Nadav

Title:

SEMICONDUCTOR INTEGRATED CIRCUIT, METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT, CHARGE PUMP CIRCUIT, LAYOUT DESIGNING APPARATUS,

AND LAYOUT DESIGNING PROGRAM

## PETITION FOR EXTENSION OF TIME

Assistant Commissioner for Patents

March 11, 2005

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

The undersigned hereby petitions for an extension of time to respond to the Official Action of January 11, 2005 for one month to March 11, 2005

Please charge the extension fee of \$120 to Deposit Account No. 25-0120. If this fee is insufficient, the Patent Office is hereby authorized to charge any additional extension fee to Deposit Account No. 25-0120. A duplicate copy of this sheet is enclosed.

A responsive paper is filed herewith.

Respectfully submitted,

YOUNG & THOMPSON

03/15/2005 MBEYENE1 00000032 250120 10648256

01 FC:1251 120.00 DA

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RJP/mjr March 11, 2005